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CONFIRMATION NO. 3385

30593
HARNESS, DICKEY & PIERCE, P.L.C.
P.O. BOX 8910
RESTON, VA20195

FILING RECEIPT

Date Mailed: 01/25/2007

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Applicant(s)

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Assignment For Published Patent Application

JURIDICAL FOUNDATION OSAKA INDUSTRIAL PROMOTION OR, Osaka,
JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 30593

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/03208 03/11/2004

Foreign Applications

JAPAN 2003-095116 03/31/2003

If Required, Foreign Filing License Granted: 01/23/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/551,266**

Projected Publication Date: 05/03/2007

Non-Publication Request: No

Early Publication Request: No

Title

Lateral bipolar cmos integrated circuit

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